

IN THE CLAIMS:

Cancel claims 2 and 5.

1. (Currently Amended) A liquid crystal display comprising:

a first substrate;

a gate line formed on the first substrate;

a data line formed on the first substrate and intersecting the gate line;

a pixel electrode disposed substantially in an area defined by the gate line and the data line;

a thin film transistor including a gate electrode connected to the gate line, a source electrode connected to the data line, and a drain electrode connected to the pixel electrode;

a second substrate facing the first substrate;

a common electrode formed on the second substrate;

a liquid crystal layer interposed between the first substrate and the second substrate;

first and second domain defining members defining a plurality of domains in the liquid crystal layer,

wherein the drain electrode is disposed near a corner of one of the domains;

wherein each domain has a pair of major edges extending parallel to each other;

and

wherein the drain electrode has a plurality of edges and the edges of the drain electrode include a first edge extending perpendicular to the major edges of the domains and located closest to a center of the one of the domains.

2. (Cancelled)

3. (Currently Amended) The liquid crystal display of claim 2 1, wherein the major edges of the domains make an angle of about 45 degrees with the gate line.

4. (Currently Amended) The liquid crystal display of claim 2 1, wherein the drain electrode has a first edge perpendicular to the major edges of the one of the domains.

5. (Cancelled)

6. (Original) The liquid crystal display of claim 1, wherein the first and the second domain defining members include cutouts provided in the pixel electrode and the common electrode.

7. (Original) The liquid crystal display of claim 1, further comprising a storage electrode overlapping the first domain defining member.